



VERSION WITH MARKINGS TO SHOW CHANGES

IN THE SPECIFICATION:

The paragraph beginning at page 1, lines 4-5, has been amended as follows:

The present invention relates to packet switching, more specifically to data stream decoding and data stream ~~analysing~~ analyzing.

The paragraph beginning at page 1, lines 7-11, has been amended as follows:

~~Related art~~ Background of the invention

In the field of data and computer communications there is an increasing need for high speed/high bandwidth products.

~~Documents~~ Prior art references relating to packet switching and more specifically to data stream decoding and pertinent to the present invention includes:

The paragraph beginning at page 1, lines 32-39, has been amended as follows:

The invention relates to a device for data stream ~~analysing~~ analyzing. Said device is able to ~~recognise~~ recognize different data streams and then start other processors or functionalities to store or check data in a data stream. Special features are: a compare processor, a compare instruction memory, a data stream pipeline, a multiplexer and a multiplexer control unit, making it possible to test packet data under program control using several instructions and under several clock cycles even though ~~said the~~ the data is moving

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